

MICRON.104DV1



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

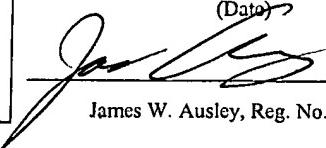
Applicant : TRIVEDI, Jigish D.
Appl. No. : 10/631,921
Filed : July 31, 2003
For : TRANSISTOR GATE AND
LOCAL INTERCONNECT
Examiner : POMPEY, Ron E.
Group Art Unit : 2812

CERTIFICATE OF MAILING

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

February 22, 2005

(Date)


James W. Ausley, Reg. No. 49,076

RESPONSE TO FINAL OFFICE ACTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Final Office Action mailed September 22, 2004, please amend the above-captioned application as follows:

Amendments to the Specification are not being made by this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to the Drawings are not being made by this paper.

Summary of Interview begins on page 7 of this paper.

Remarks/Arguments begin on page 8 of this paper.